

#2 IDS
Opposition
1/16/02

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Parekh et al.

Serial No.: Not yet assigned

Filed: August 30, 2001

For: METHOD OF REDUCING
ELECTRICAL SHORTS FROM THE BIT
LINE TO THE CELL PLATE

Examiner: Unknown

Group Art Unit: Unknown

Attorney Docket No.: 4475.1US (98-1097.1)

11000 U.S. PTO
09/943778
08/30/01

NOTICE OF EXPRESS MAILING

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Person making Deposit: Jared Turner

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D.C. 20231

Sir:

The present application is a divisional of application Serial No. 09/399,592, filed September 20, 1999, pending.

Pursuant to M.P.E.P. 2001.06(b), the Examiner is respectfully requested to consider the information of record in the prior application, and to confirm in the first Office Action on the merits that such art has in fact been reviewed. A PTO-1449 form listing all of the information of record in the prior application is enclosed herewith.

This Information Disclosure Statement is filed within three (3) months of the filing date of the above-identified application, and no certification pursuant to 37 C.F.R. § 1.97(c) or a fee pursuant to 37 C.F.R. 1.17(p) is required.

Respectfully submitted,



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Date: August 30, 2001

DLS/sls:djp

Enclosure: Form PTO-1449